

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 (Use several sheets if necessary)				ATTY. DOCKET NO. PD030014		SERIAL NO.	
				APPLICANTS Jens Peter Wittenburg et al.			
				FILING DATE Herewith		GROUP	

U.S. PATENT DOCUMENTS							
EXAMINE INITIAL	DOCUMENT NUMBER	ISSUE DATE	APPLICANT/PATENTEE	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE	
	AA	5,185,868	Tran				
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						

FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	PUBL. DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION Yes No	
	AI	0039669	PCT				
	AJ						
	AK						
	AL						
	AM						
	AN						

OTHER INFORMATION (Including Author, Title, Pub.Date, Pertinent Pages, Country, Etc.)		
AO		Yoshida et al, "A Strategy for Avoiding Pipeline Interlock Delays in a Microprocessor", Proceedings of the International Conference on Computer Design: VLSI in Computers and Processors, September 17-19 1990, pages 14-19
AP		Search Report for EP Appln. No. 03090089 dated July 31, 2003
AQ		

EXAMINER	DATE CONSIDERED

SUBMITTED BY: Reitseng Lin			REG. NO.: 42,804	DATE: March 29, 2004
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